

ZZZ
DAB@
DA80000QD00
PCB

ZZZ1
EL@
X76346BOL01

ZZZ2
HY@
X76346BOL02

S1
ME@
EC0NY000600

Compal Confidential

QCJ00(LA-8111P) Schematics Document

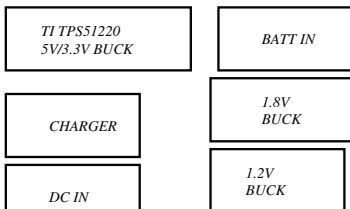
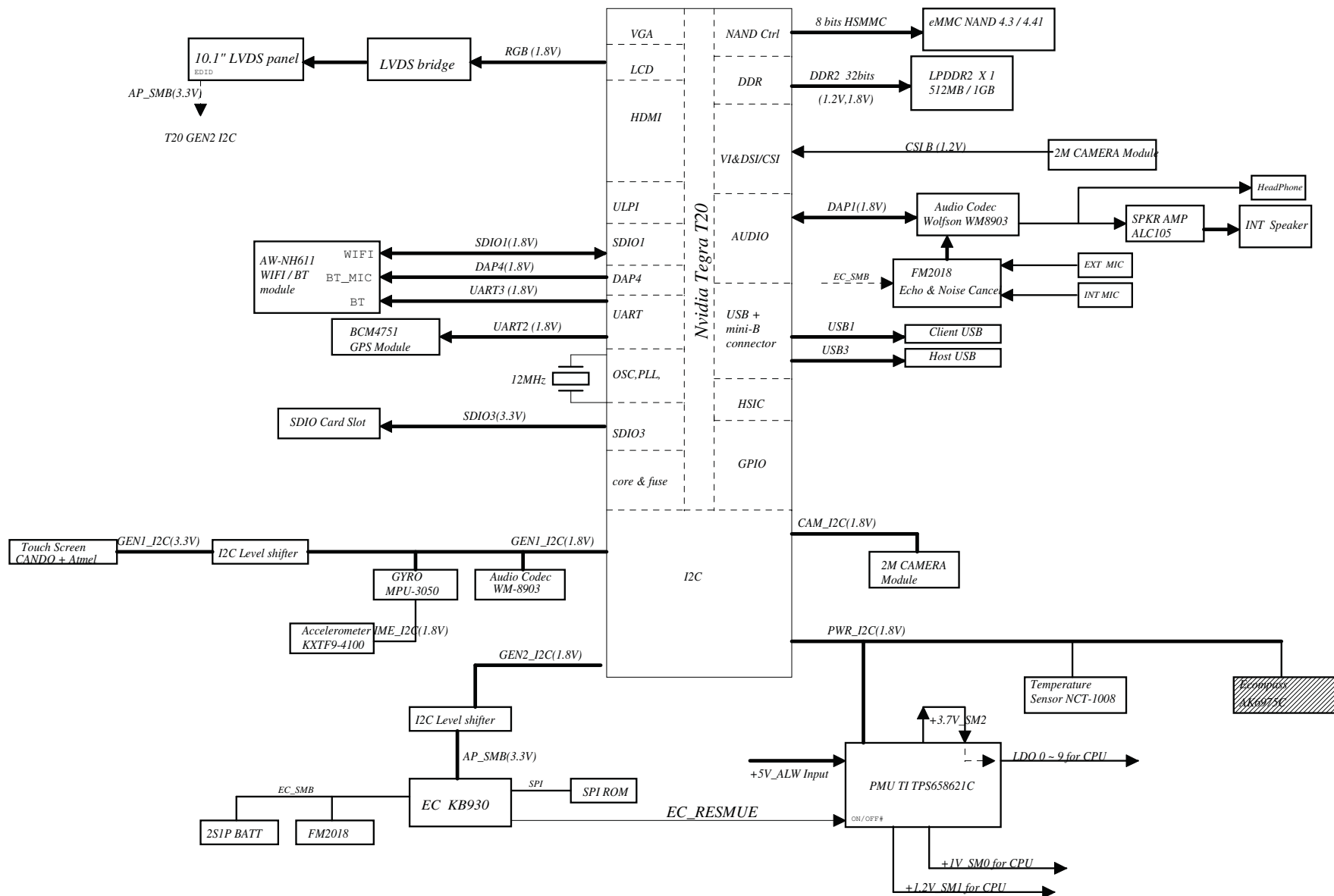
Nvdia(T20) + LPDDRII

2011-09-30

REV: 0.3

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QCJ00 (Picasso-E) system block diagram



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Voltage Rails

Power Plane	Description	IDLE	Standby
VIN	Adapter power supply (12V)	ON	ON
B+	AC or battery power rail for power circuit.	ON	ON
+1.2VS_SM0	Core voltage for CPU	ON	OFF
+1.0VS_SM1	CPU voltage for CPU	ON	OFF
+1.1VS_LDO1	AVDD_PLL power rail	ON	OFF
+1.2VS_LDO2	T20 RTC power rail	ON	OFF
+1.8VS_LDO4	T20 system power rail	ON	ON
+3.3VS_LDO3	T20 USB power rail	ON	OFF
+2.85VS_LDO5	Core voltage for EMMC	ON	OFF
+2.85VS_LDO6	Core voltage for CAMERA	ON	OFF
+3.3VS_LDO7	T20 HDMI power rail	ON	OFF
+1.8VS_LDO8	T20 HDMI PLL power rail	ON	OFF
+2.85VS_LDO9	T20 DDR RX power rail	ON	ON
+3VALW	3.3V always on power rail	ON	ON
+3VS	3.3V switched power rail for standby mode	ON	ON
+5VALW	5V always on power rail	ON	ON
+1.8V	1.8V always on power rail	ON	ON
+1.8VS	1.8V switched power rail for standby mode	ON	ON
+3.3VS_RTC	RTC power	ON	ON

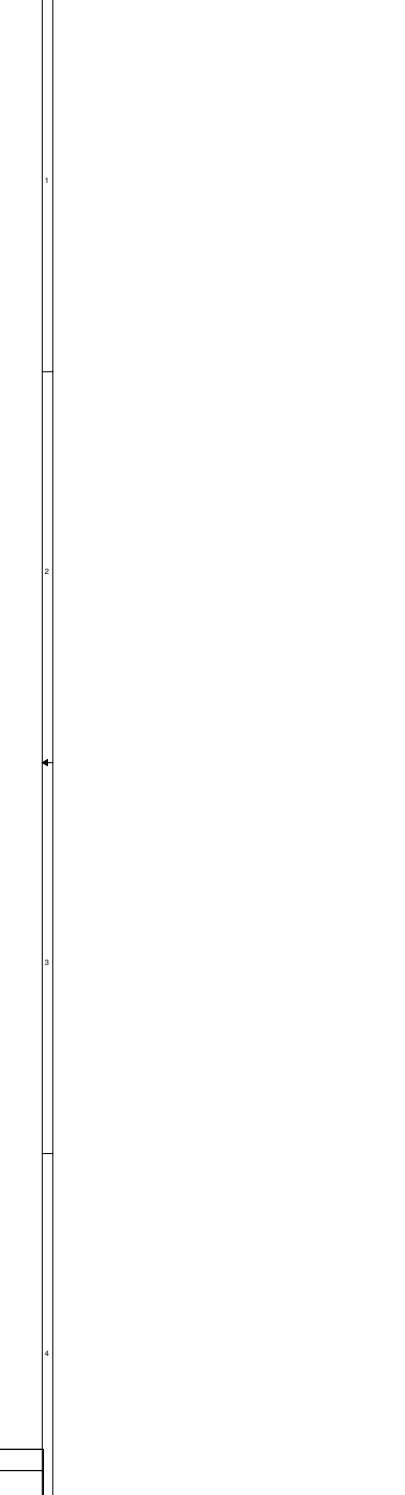
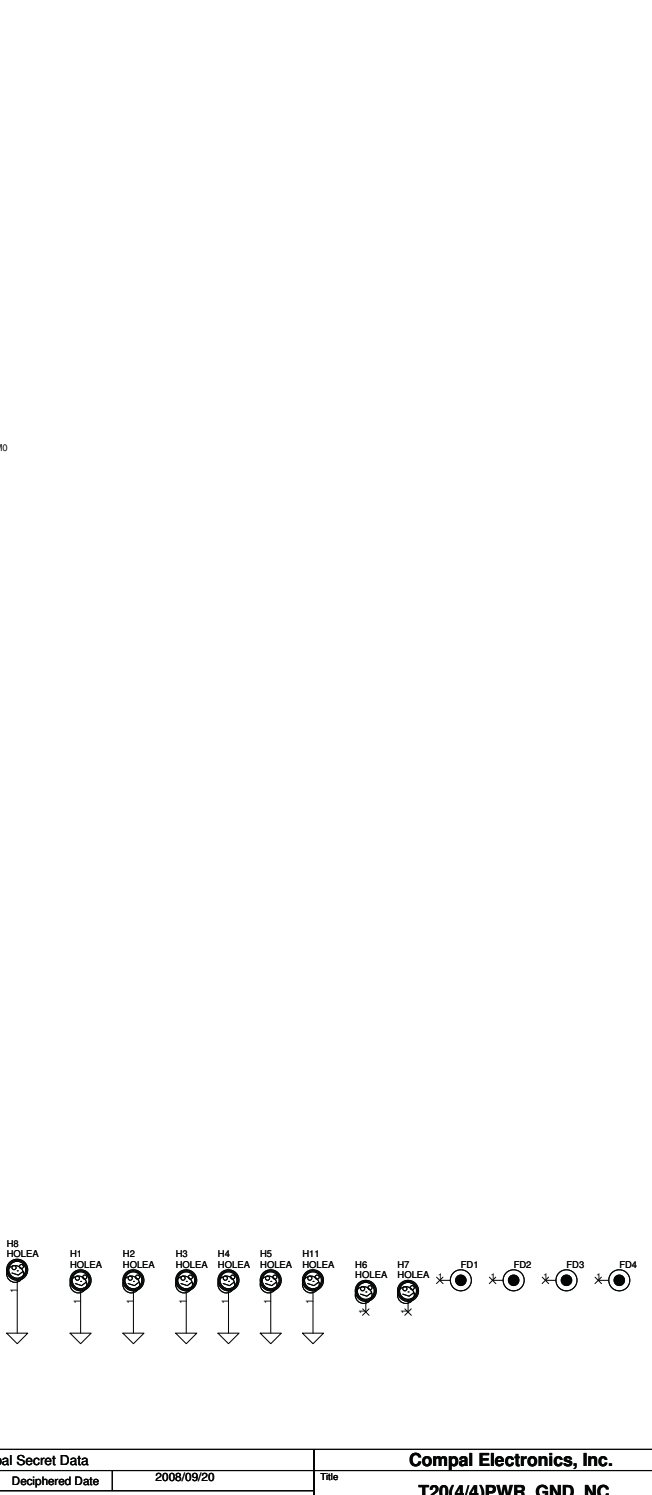
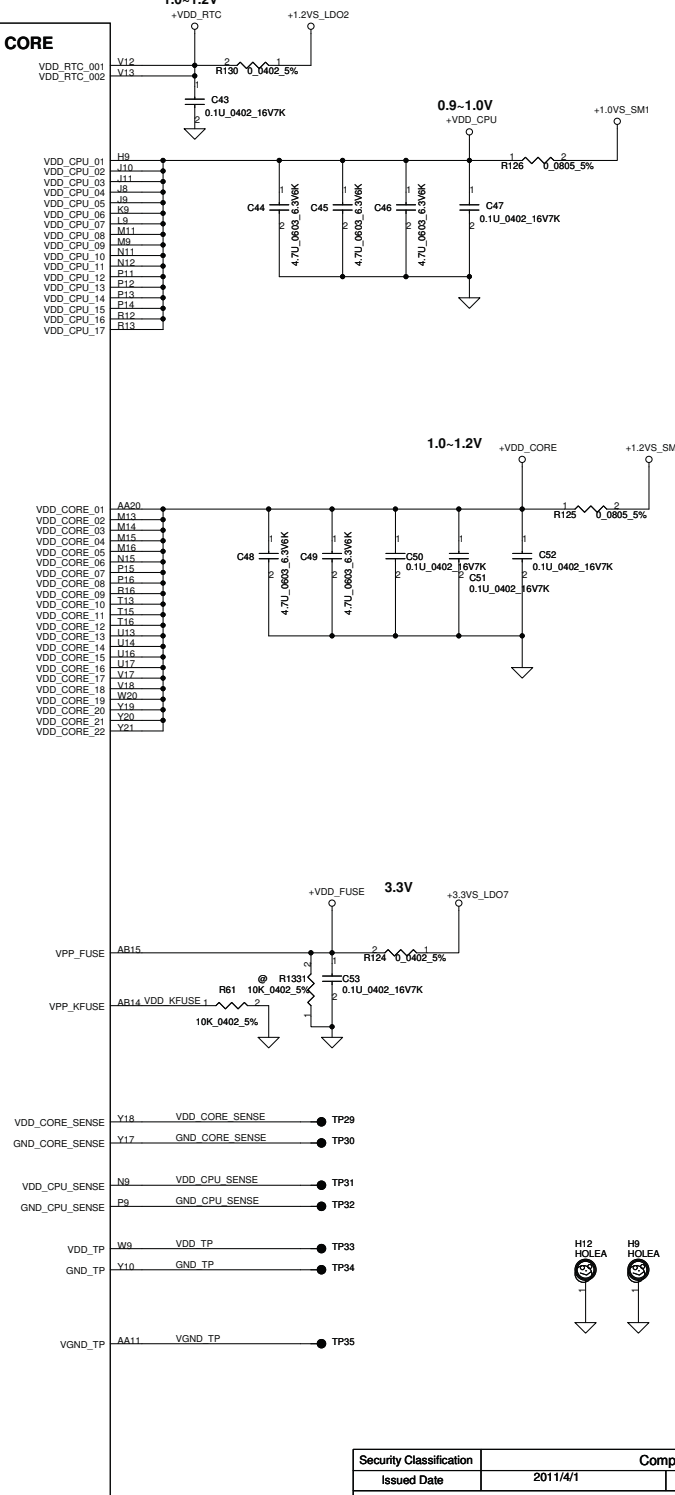
SIGNAL STATE	EC_SYS_STANDBY#	EN_VDD_+1.8VS_S3	EN_VDD_3V3	EN_VDD_1V8
Full ON	High	High	High	High
Standby	High	High	High	High

GEN1_I2C	TS_I2C
Audio Codec 1001 010xb	Device
G-sensor 0000 1111 b	Touch Panel
Gyro 1101 000x b	0x4C or 0x4D
GEN2_I2C	AP_SMB
	Device
	PANEL EDID
EC_SMB	EC
	Device
	Address
Echo Cancellation	1011 111xb
BATT	
CAM_I2C address	
	Device
	Address
CAMERA 1.3M	1001 000X b
PWR_I2C address	
	Device
	Address
PMU	0110 100X b
Temperature sensor	0101 110X b
Ecompass	

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				Size	Document Number	Rev
				Custom		0.1
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- U1A
- CORE**
- A1 GND 001
 - A2 GND 002
 - A27 GND 003
 - A28 GND 004
 - AA10 GND 005
 - AA13 GND 006
 - AA16 GND 007
 - AA19 GND 008
 - AA21 GND 009
 - AA3 GND 010
 - AA9 GND 011
 - AB2 GND 012
 - AB24 GND 013
 - AB27 GND 014
 - AB5 GND 015
 - AC19 GND 016
 - AD10 GND 017
 - AD13 GND 018
 - AD16 GND 019
 - AD19 GND 020
 - AD22 GND 021
 - AD7 GND 022
 - AE2 GND 023
 - AE29 GND 024
 - AE27 GND 025
 - AE4 GND 026
 - AG1 GND 027
 - AG10 GND 028
 - AG13 GND 029
 - AG16 GND 030
 - AG19 GND 031
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 - AG22 GND 033
 - AG25 GND 034
 - AG27 GND 035
 - AG39 GND 036
 - AG4 GND 037
 - AG7 GND 038
 - AH1 GND 039
 - AH2 GND 040
 - AH27 GND 041
 - AH29 GND 042
 - B1 GND 043
 - B10 GND 044
 - B13 GND 045
 - B16 GND 046
 - B19 GND 047
 - B2 GND 048
 - B22 GND 049
 - B25 GND 050
 - B27 GND 051
 - B29 GND 052
 - B4 GND 053
 - B7 GND 054
 - D2 GND 055
 - D25 GND 056
 - D27 GND 057
 - D4 GND 058
 - E10 GND 059
 - E13 GND 060
 - E16 GND 061
 - E19 GND 062
 - E22 GND 063
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 - K21 GND 077
 - K24 GND 078
 - K27 GND 079
 - K5 GND 080
 - K8 GND 081
 - L11 GND 082
 - L12 GND 083
 - L13 GND 084
 - L14 GND 085
 - L15 GND 086
 - L18 GND 087
 - L8 GND 088
 - M12 GND 089
 - N13 GND 090
 - N14 GND 091
 - N16 GND 092
 - N17 GND 093
 - N2 GND 094
 - N21 GND 095
 - N24 GND 096
 - N27 GND 097
 - N5 GND 098
 - N8 GND 099
 - P17 GND 100
 - P18 GND 101
 - P11 GND 102
 - P14 GND 103
 - P15 GND 104
 - P17 GND 105
 - P18 GND 106
 - T11 GND 107
 - T12 GND 108
 - T14 GND 109
 - T17 GND 110
 - T18 GND 111
 - T2 GND 112
 - T21 GND 113
 - T24 GND 114
 - T27 GND 115
 - T6 GND 116
 - T8 GND 117
 - U11 GND 118
 - U12 GND 119
 - U15 GND 120
 - U18 GND 121
 - U21 GND 122
 - V11 GND 123
 - V14 GND 124
 - V15 GND 125
 - V16 GND 126
 - V21 GND 127
 - W2 GND 128
 - W21 GND 129
 - W24 GND 130
 - W27 GND 131
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 - W8 GND 133
 - Y11 GND 134
 - Y12 GND 135
 - Y13 GND 136



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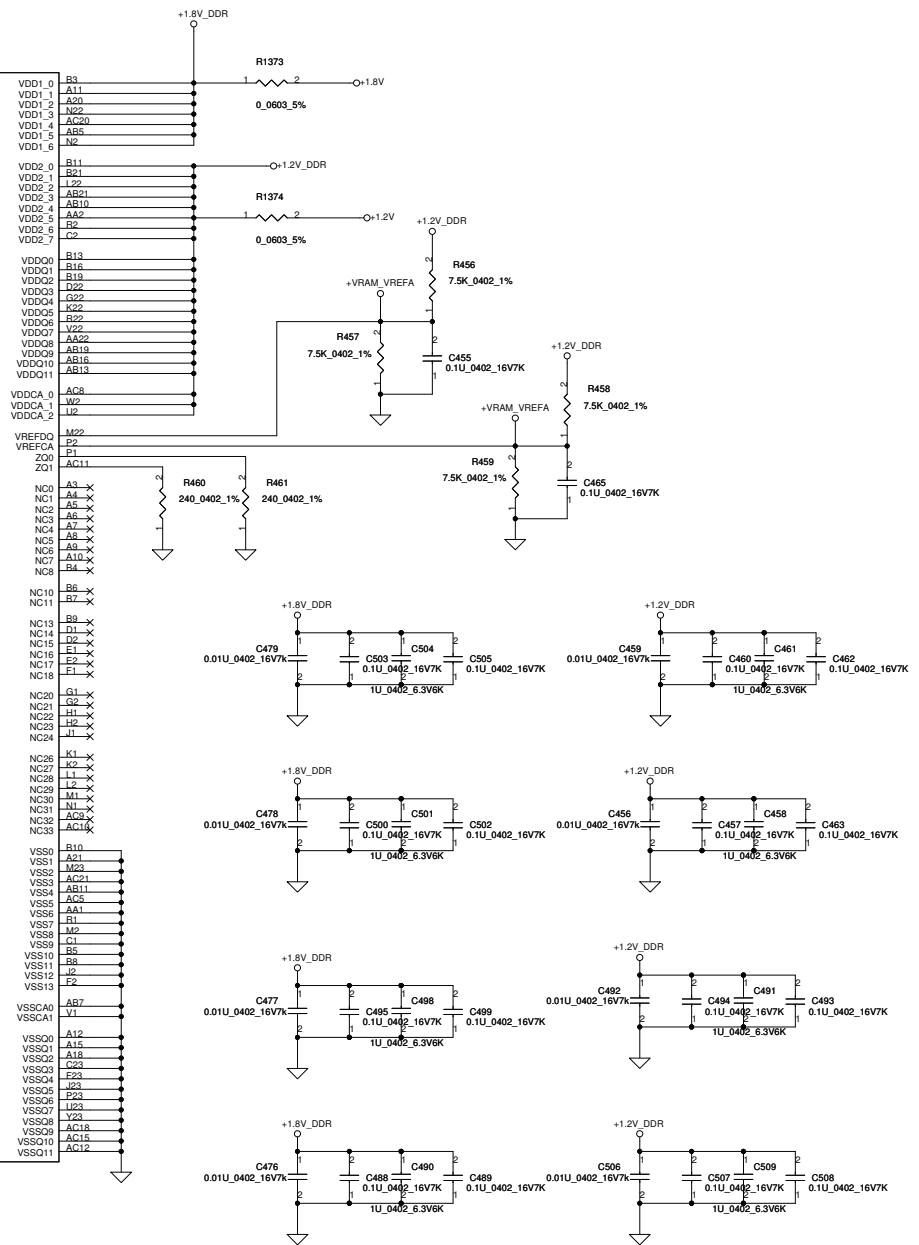
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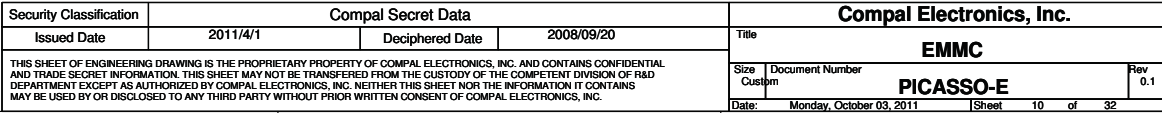
U129

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5 DDR_A_MA8	DDR_A_MA8	T2	CA8
5 DDR_A_MA7	DDR_A_MA7	U1	CA7
5 DDR_A_MA6	DDR_A_MA6	V2	CA6
5 DDR_A_MA5	DDR_A_MA5	W1	CA5
5 DDR_A_MA4	DDR_A_MA4	AB9	CA4
5 DDR_A_MA3	DDR_A_MA3	AB8	CA3
5 DDR_A_MA2	DDR_A_MA2	AC7	CA2
5 DDR_A_MA1	DDR_A_MA1	AB6	CA1
5 DDR_A_MA0	DDR_A_MA0	AC6	CA0
5 M_CS#0	M_CS#0	AB3	CS0#
5 M_CS#1	M_CS#1	AB4	CS1#
5 M_CLK_DDR0	M_CLK_DDR0	Y2	CK
5 M_CLK_DDR#0	M_CLK_DDR#0	X1	CK#
5 M_CKE1	M_CKE1	AC3	CKE0
5 M_CKE1	M_CKE1	AC4	CKE1
5 DDR_A_DQS3	DDR_A_DQS3	B18	DQS3
5 DDR_A_DQS#3	DDR_A_DQS#3	A19	DQS3#
5 DDR_A_DQS2	DDR_A_DQS2	AB18	DQS2
5 DDR_A_DQS#2	DDR_A_DQS#2	AC19	DQS2#
5 DDR_A_DQS1	DDR_A_DQS1	J22	DQS1
5 DDR_A_DQS#1	DDR_A_DQS#1	K23	DQS1#
5 DDR_A_DQS0	DDR_A_DQS0	R23	DQS0
5 DDR_A_DQS#0	DDR_A_DQS#0	P22	DQS0#
5 DDR_A_DM3	DDR_A_DM3	R20	DM3
5 DDR_A_DM2	DDR_A_DM2	AB20	DM2
5 DDR_A_DM1	DDR_A_DM1	L23	DM1
5 DDR_A_DM0	DDR_A_DM0	N23	DM0
5 DDR_A_D31	DDR_A_D31	B12	DQ31
5 DDR_A_D30	DDR_A_D30	A13	DQ30
5 DDR_A_D29	DDR_A_D29	B14	DQ29
5 DDR_A_D28	DDR_A_D28	B15	DQ28
5 DDR_A_D27	DDR_A_D27	A16	DQ27
5 DDR_A_D26	DDR_A_D26	A17	DQ26
5 DDR_A_D25	DDR_A_D25	A17	DQ25
5 DDR_A_D24	DDR_A_D24	B17	DQ24
5 DDR_A_D23	DDR_A_D23	AC17	DQ23
5 DDR_A_D22	DDR_A_D22	AB17	DQ22
5 DDR_A_D21	DDR_A_D21	AC16	DQ21
5 DDR_A_D20	DDR_A_D20	AB15	DQ20
5 DDR_A_D19	DDR_A_D19	AC14	DQ19
5 DDR_A_D18	DDR_A_D18	AB14	DQ18
5 DDR_A_D17	DDR_A_D17	AC13	DQ17
5 DDR_A_D16	DDR_A_D16	AB12	DQ16
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5 DDR_A_D6	DDR_A_D6	T22	DQ6
5 DDR_A_D5	DDR_A_D5	U22	DQ5
5 DDR_A_D4	DDR_A_D4	V23	DQ4
5 DDR_A_D3	DDR_A_D3	W23	DQ3
5 DDR_A_D2	DDR_A_D2	W22	DQ2
5 DDR_A_D1	DDR_A_D1	Y22	DQ1
5 DDR_A_D0	DDR_A_D0	AB23	DQ0

X A1 DNU0
 X A2 DNU1
 X A22 DNU2
 X A23 DNU3
 X B1 DNU4
 X B2 DNU5
 X B22 DNU6
 X B23 DNU7
 X AB1 DNU8
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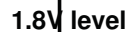
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7/1 del D26

U10



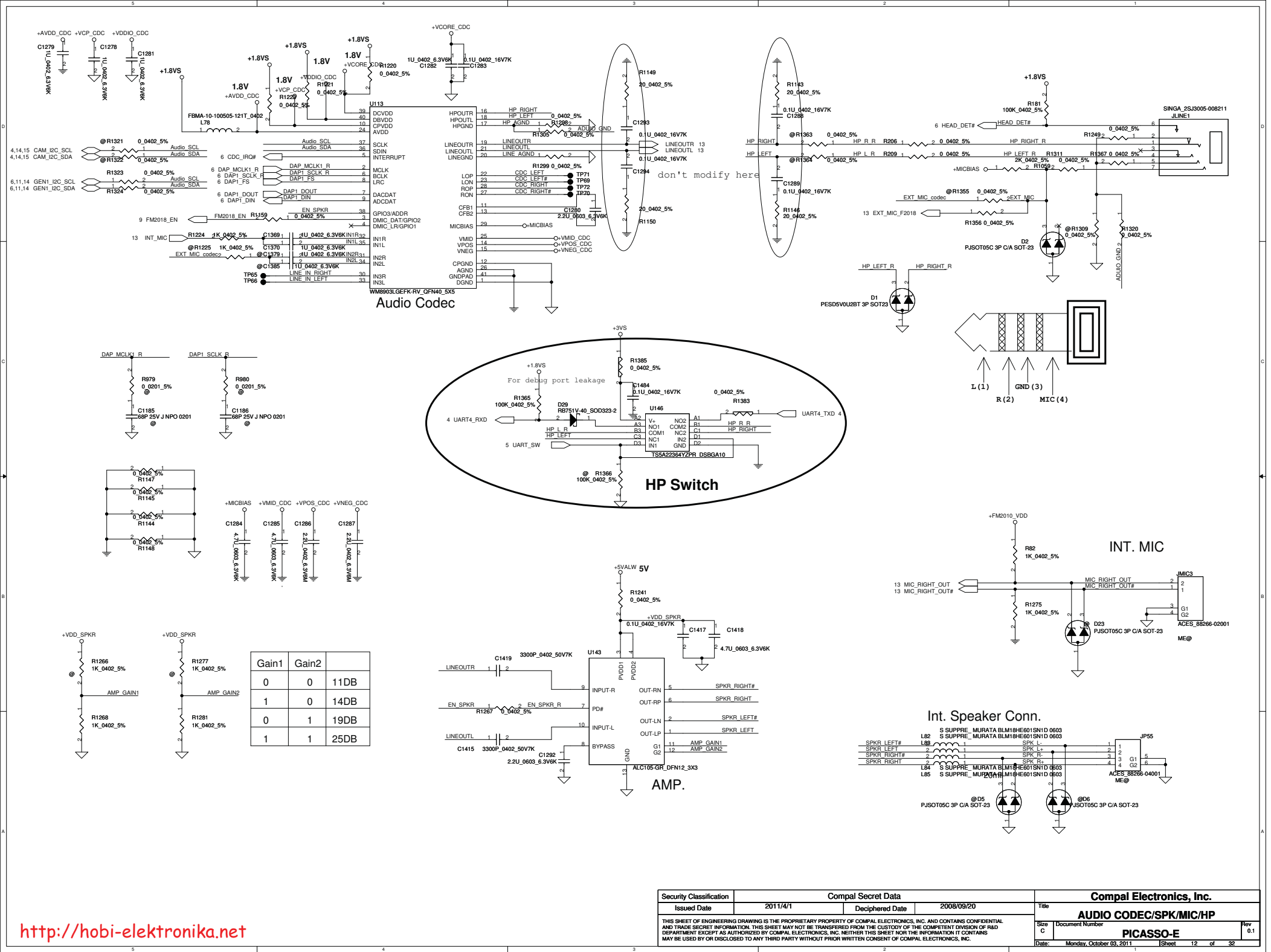
LCD POWER CIRCUIT

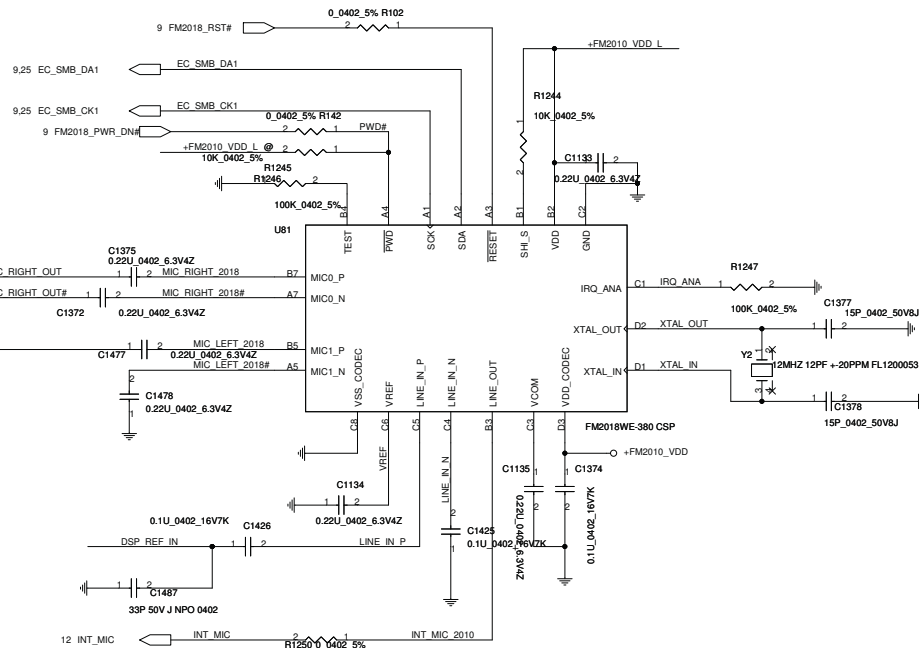
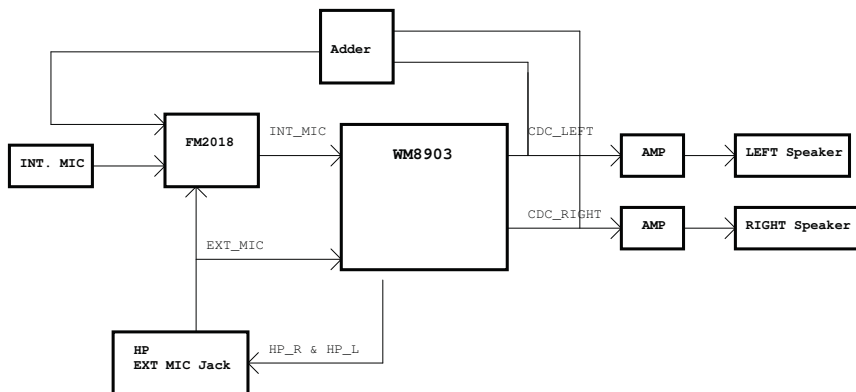
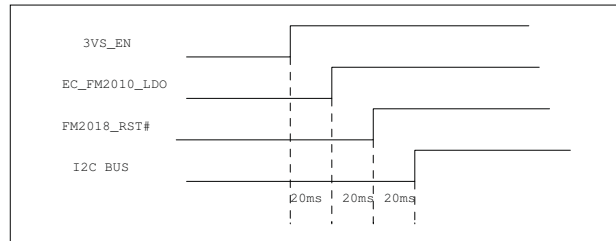
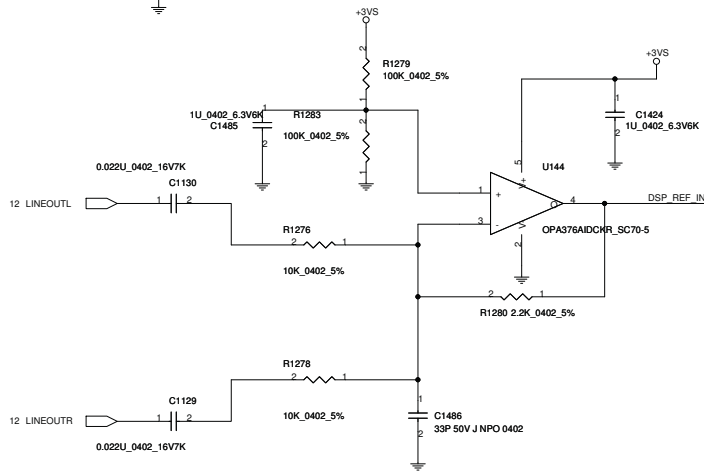
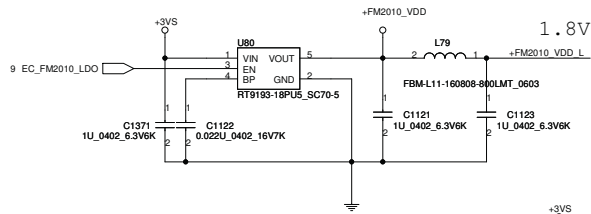
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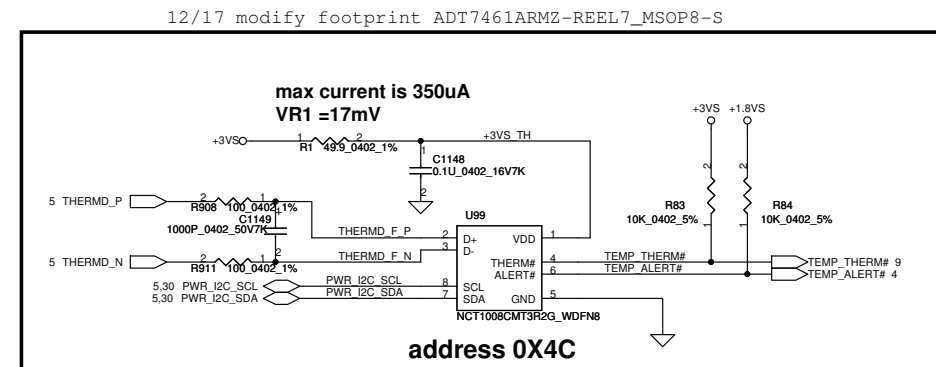
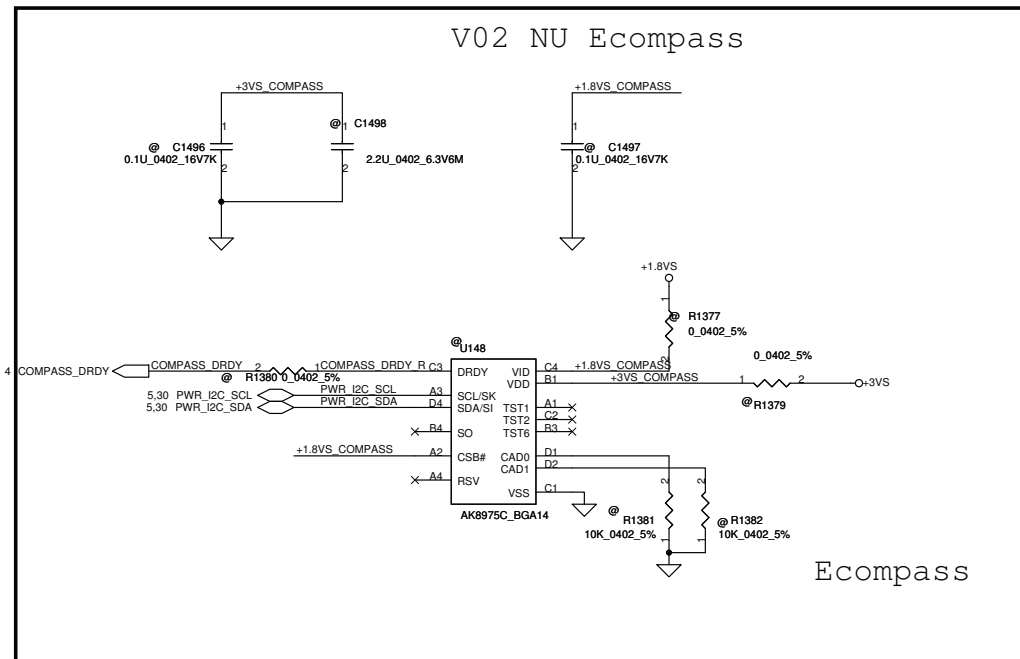
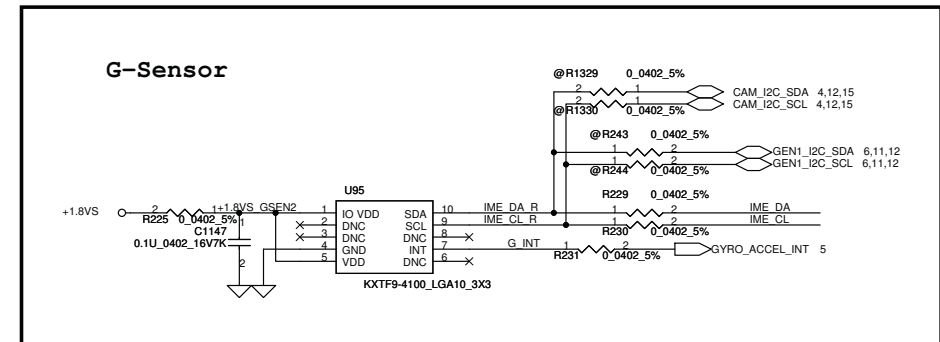
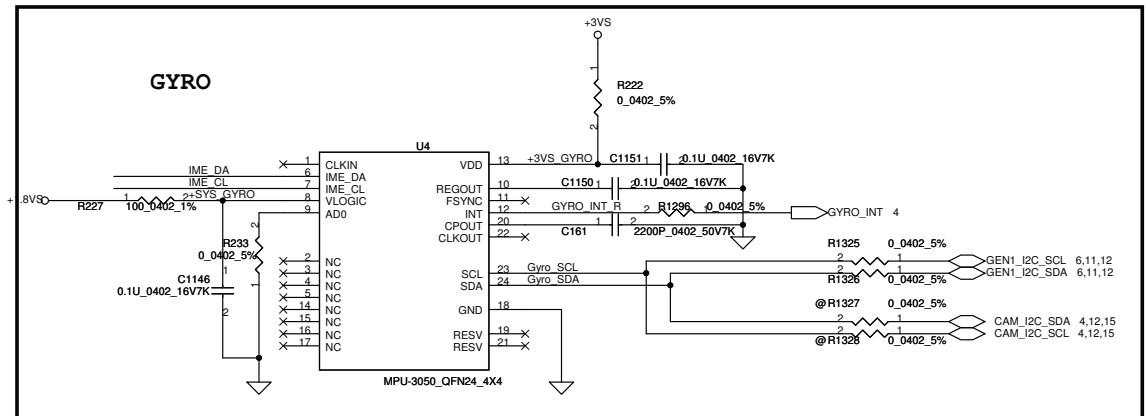
Level shift

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LVDS A01#	C104	1	2	12P	0402_50V4Z
LVDS A02	C105	1	2	12P	0402_50V4Z
LVDS A02#	C106	1	2	12P	0402_50V4Z
LVDS ACLK0	C107	1	2	12P	0402_50V4Z
LVDS ACLK0#	C108	1	2	12P	0402_50V4Z

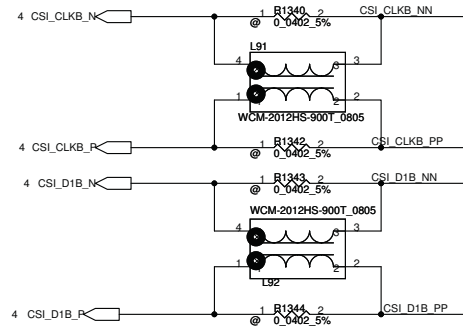
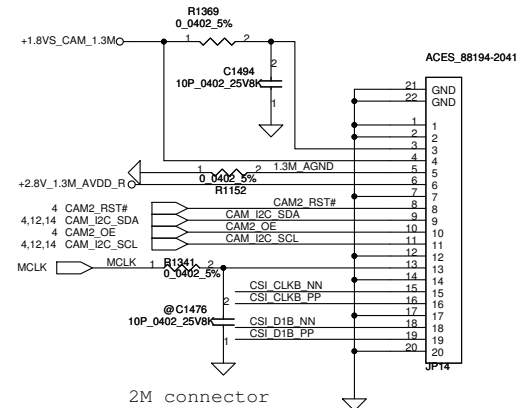
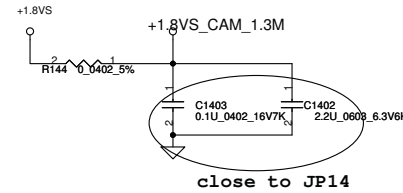
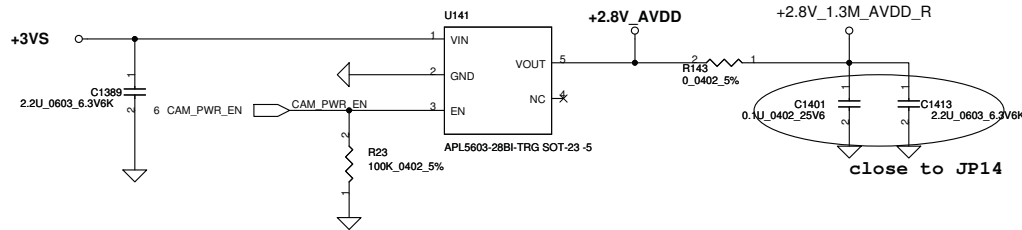
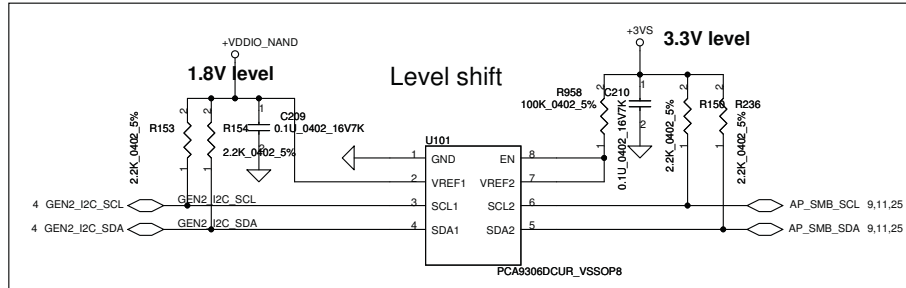
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						Custm				0.1
						Date:		Monday, October 03, 2011		Sheet



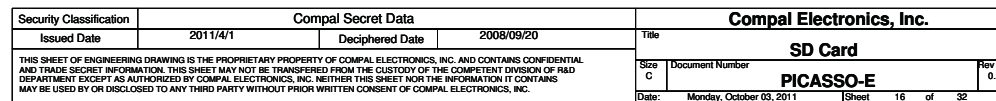
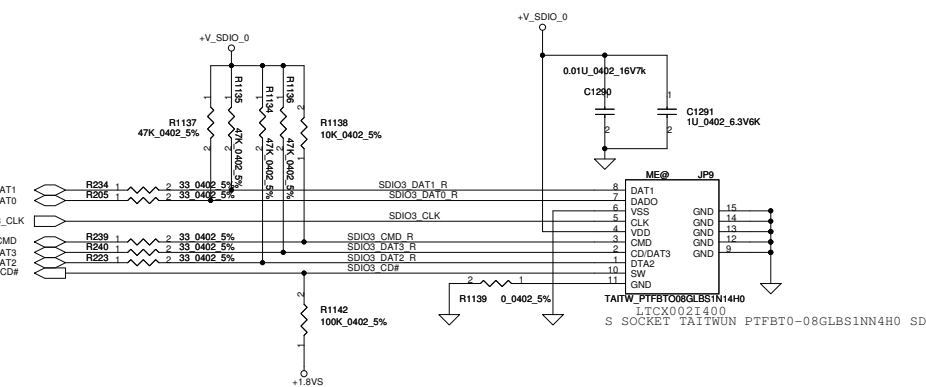


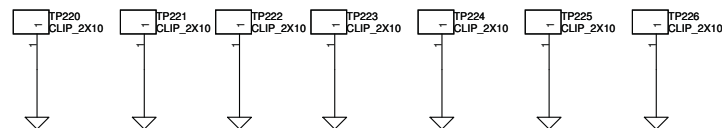
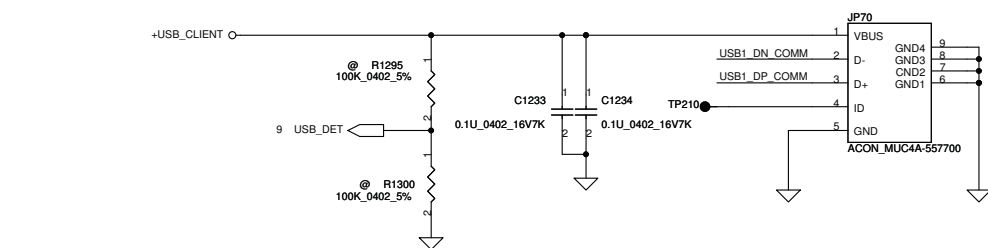
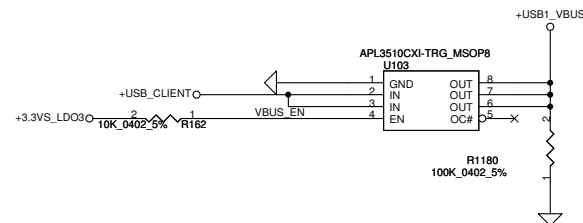
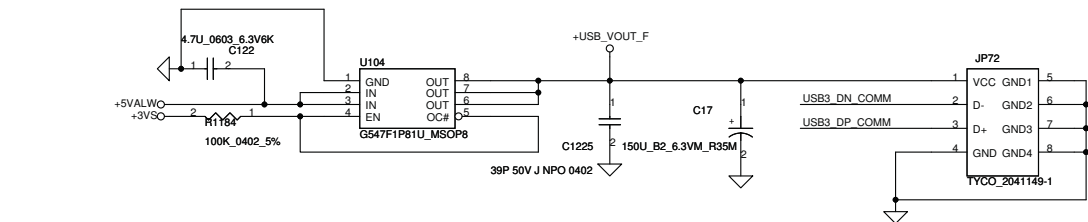
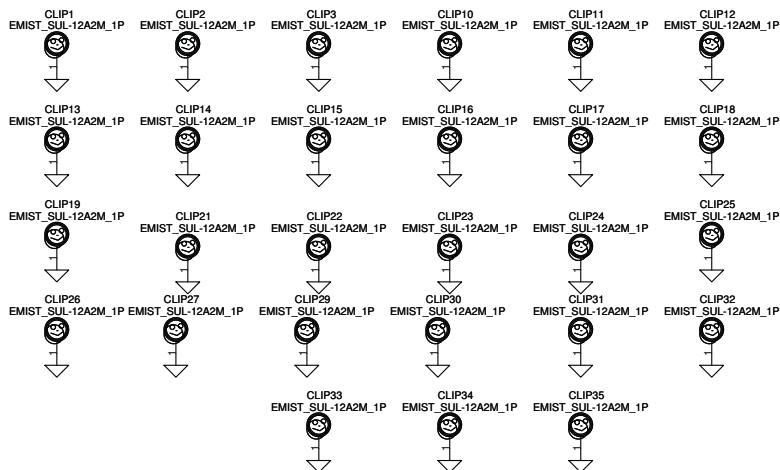
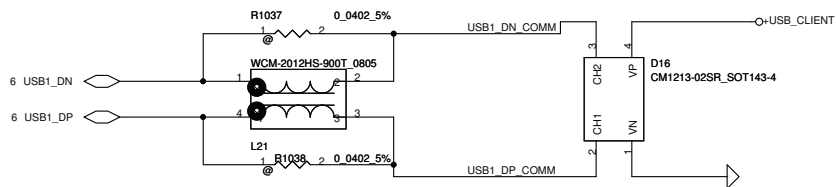
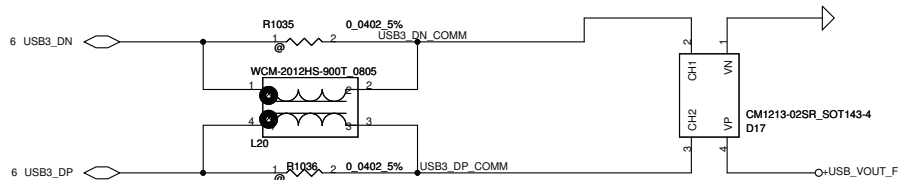


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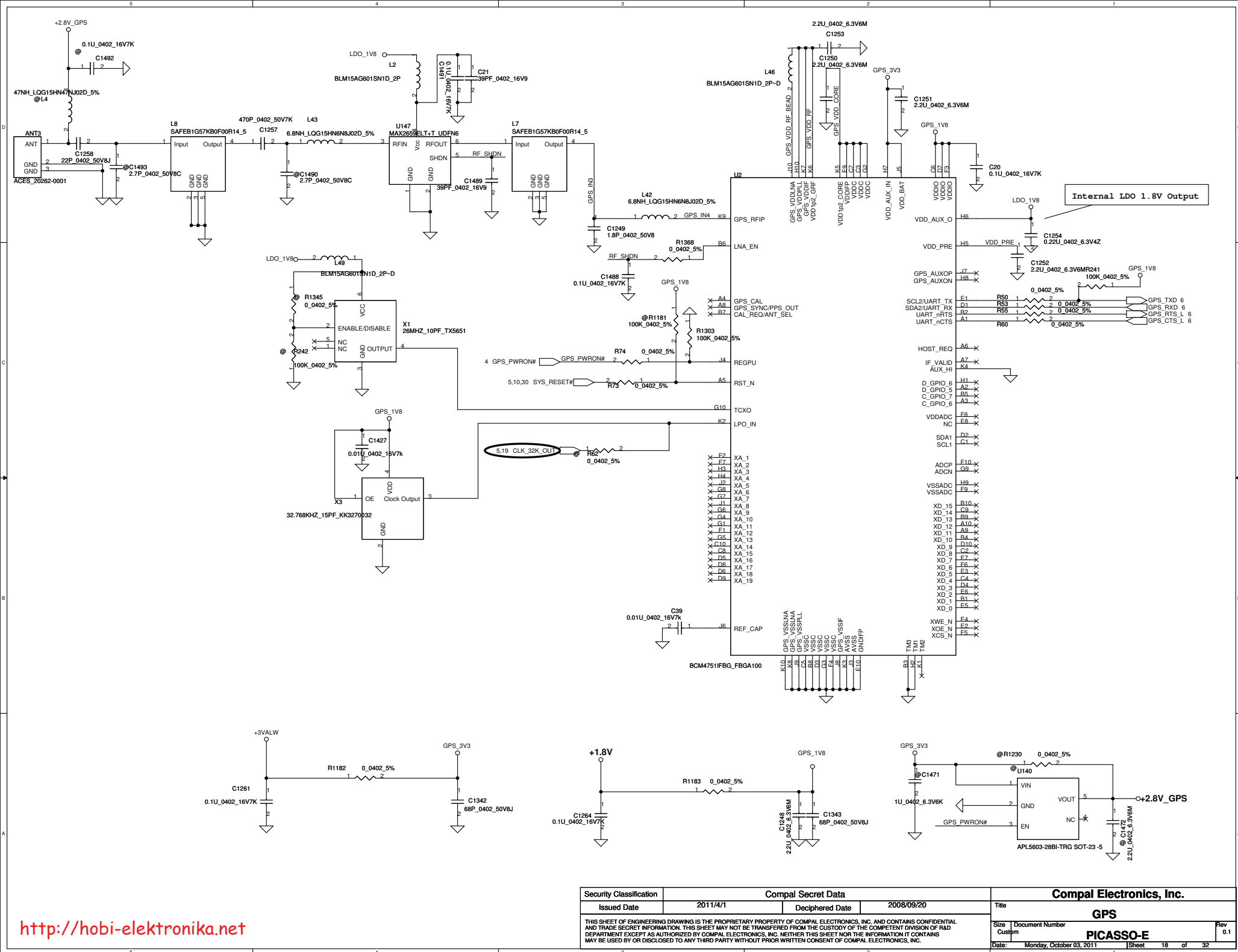


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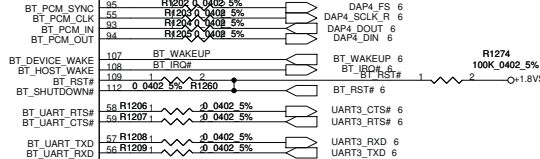
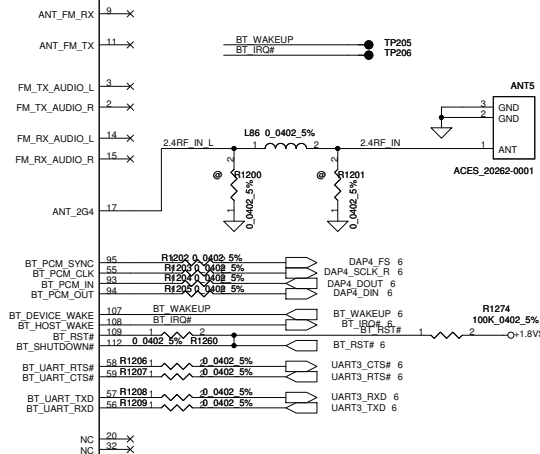
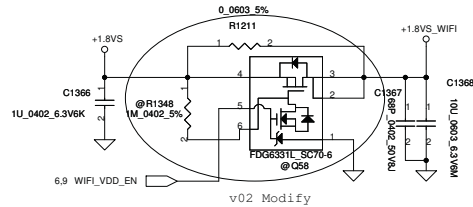
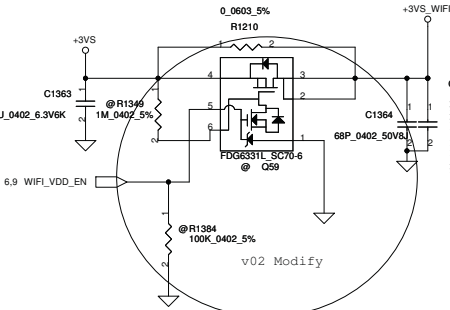
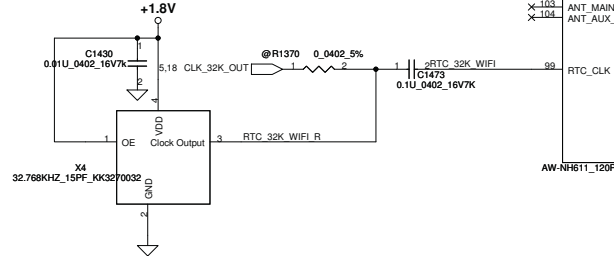
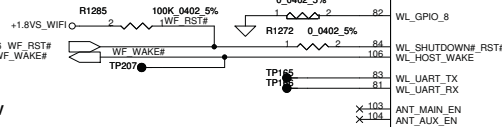
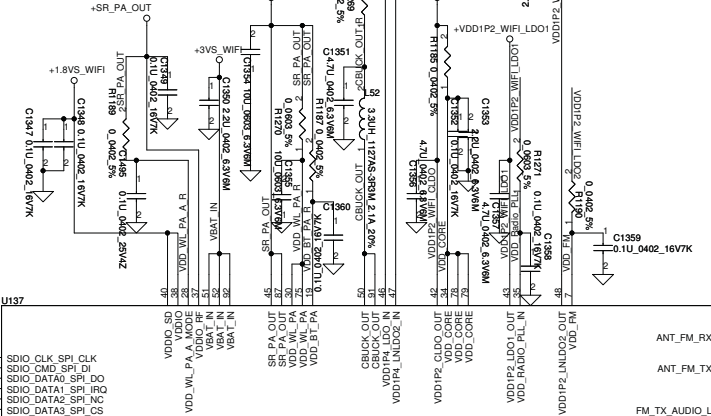
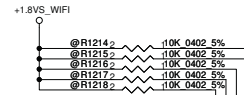
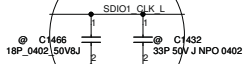




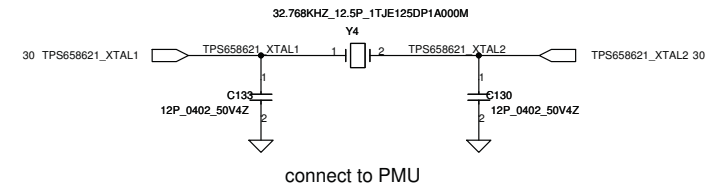
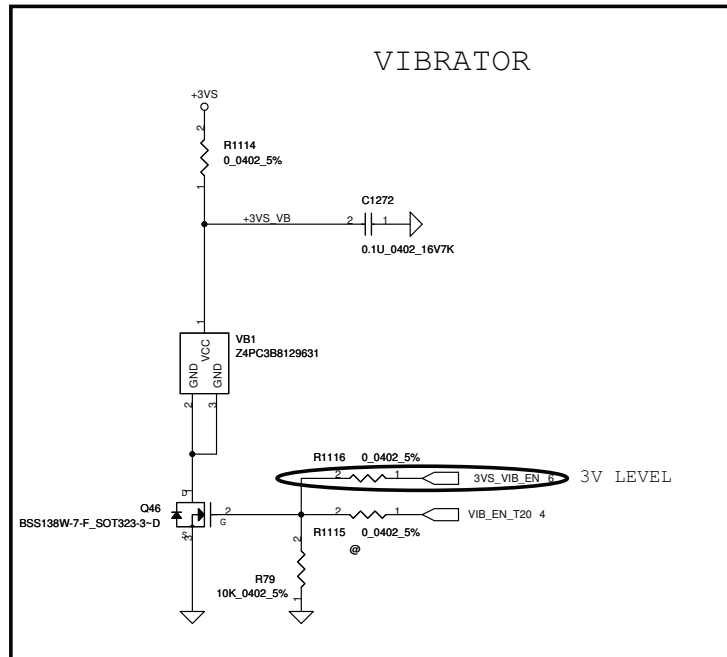
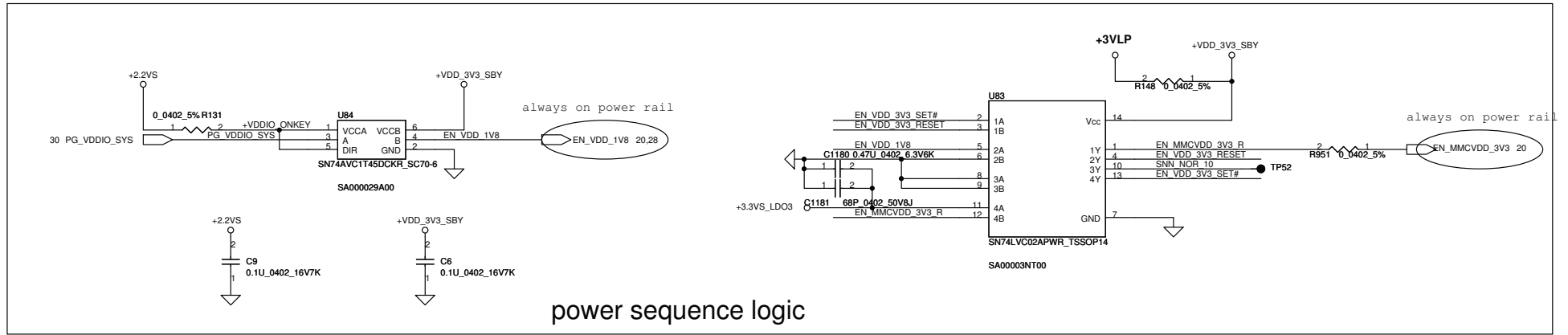
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				Size	Rev
				Custom	0.1
				PICASSO-E	
				Date:	Monday, October 03, 2011
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RF request

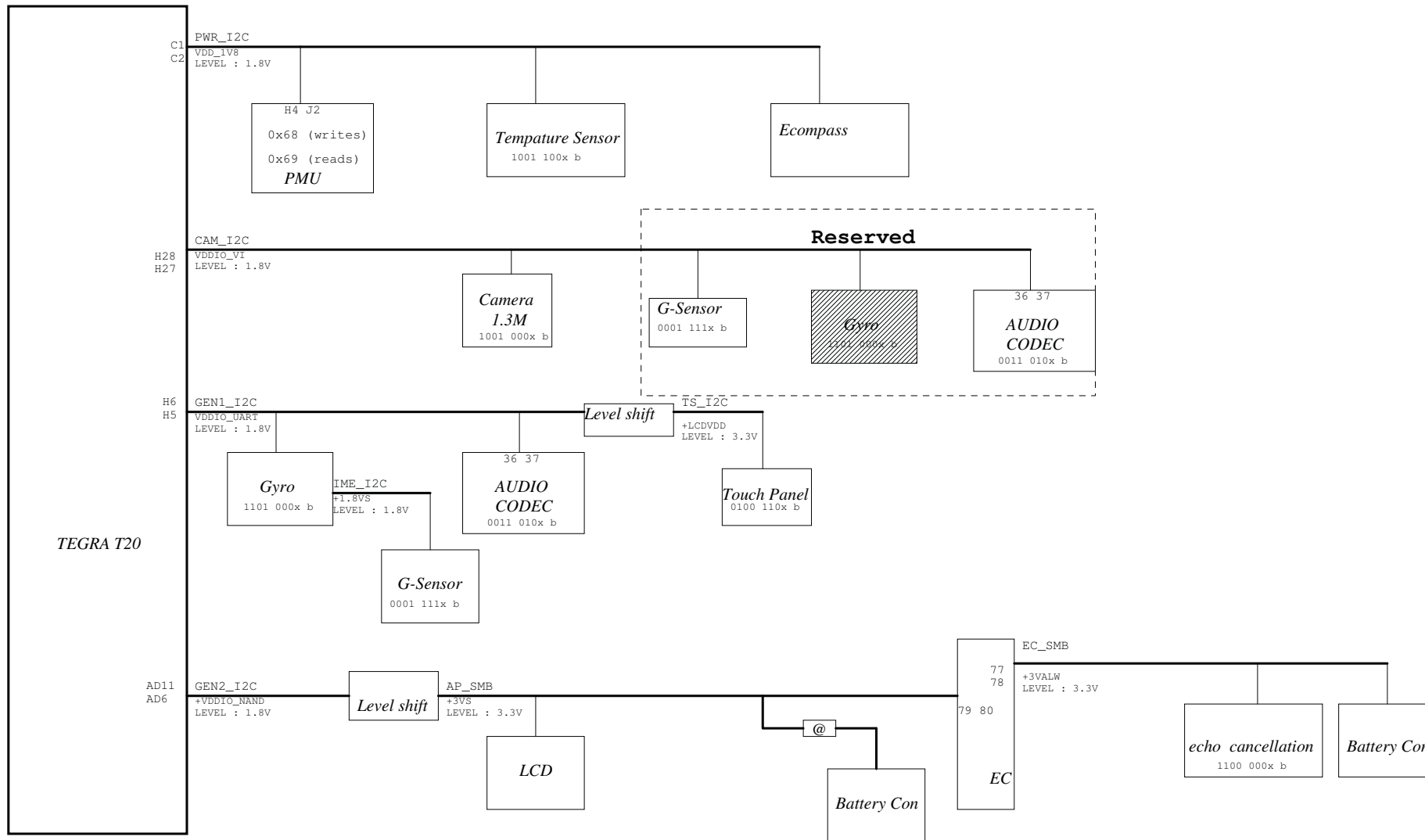


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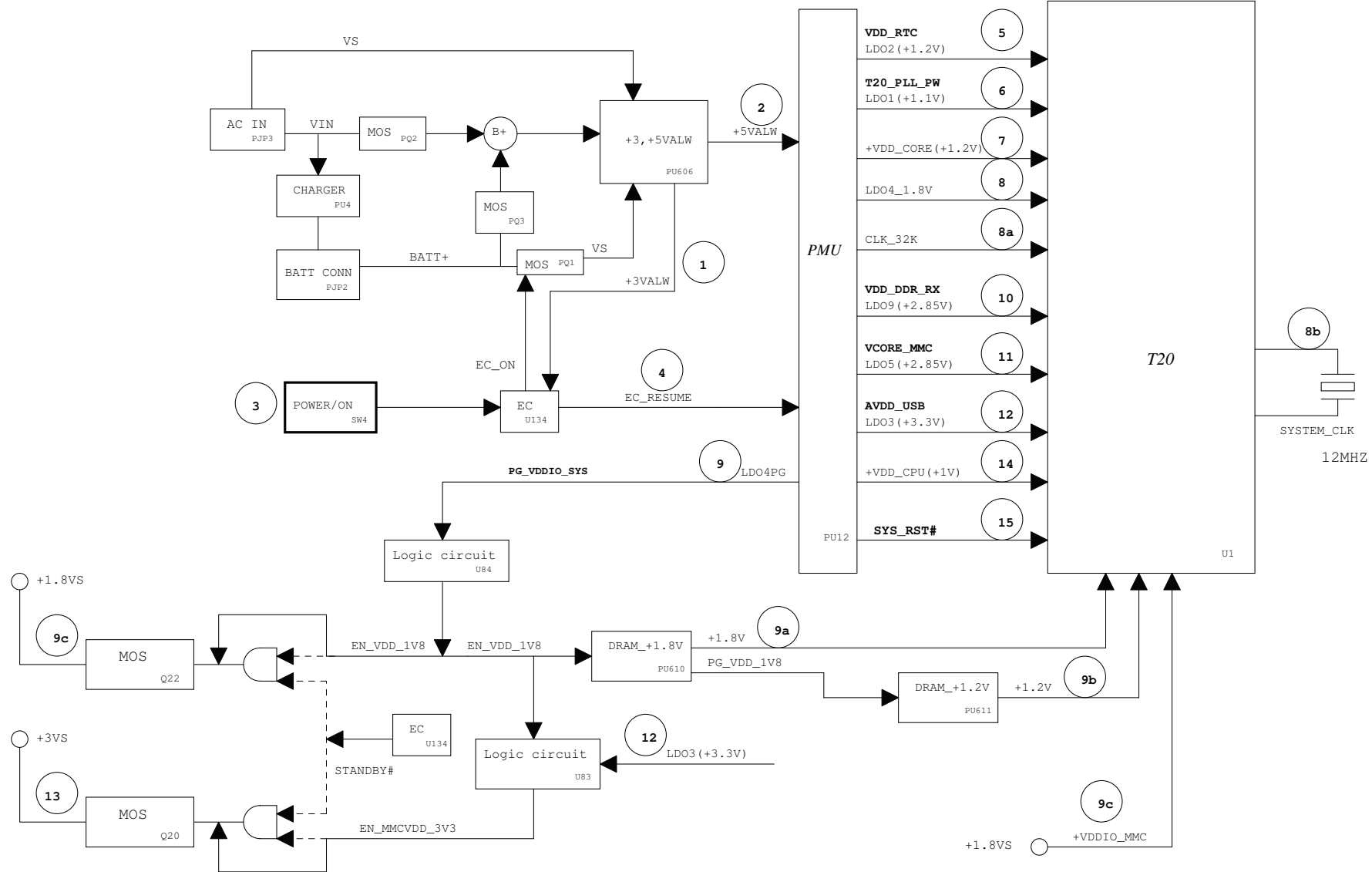
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Size	Document Number	PICASSO-E		Rev	0.1
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PICASSO-E I2C Routing

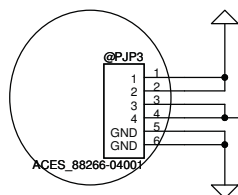


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Size	C	Document Number	PICASSO-E	Rev 8.1
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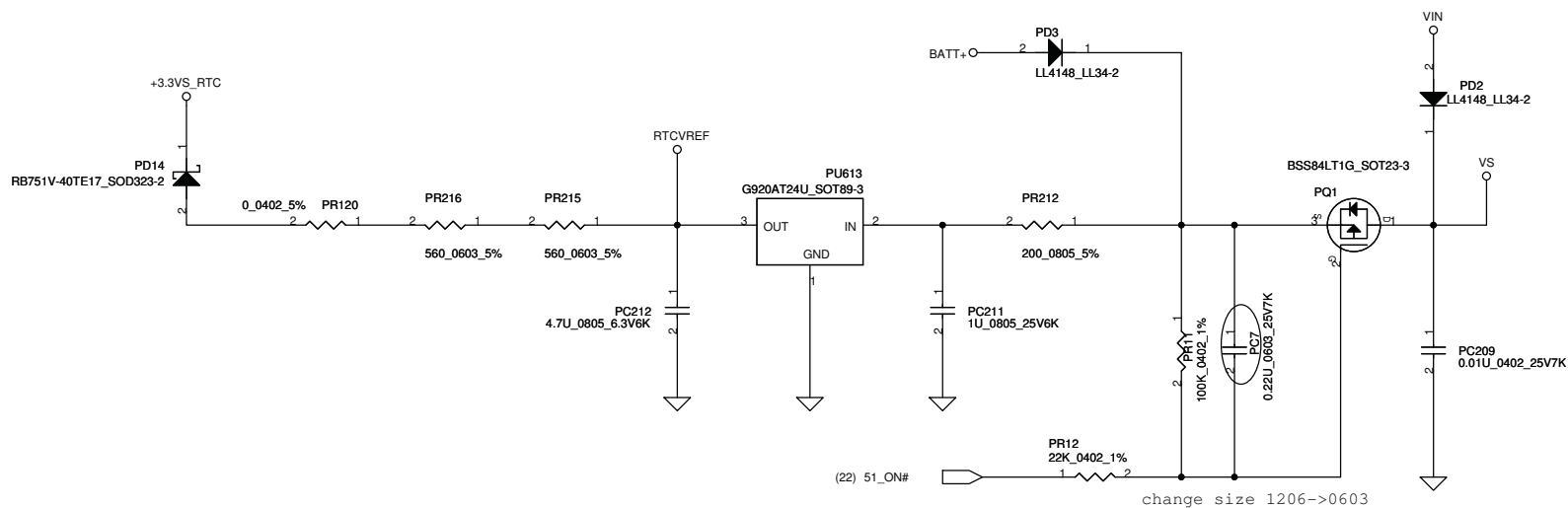
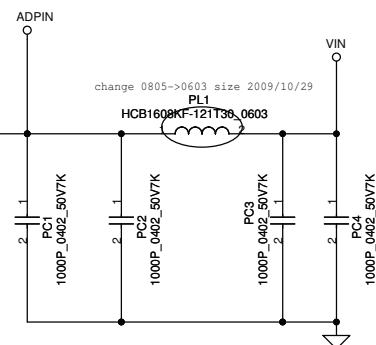
QCJ00 Power Sequence Flow Chart



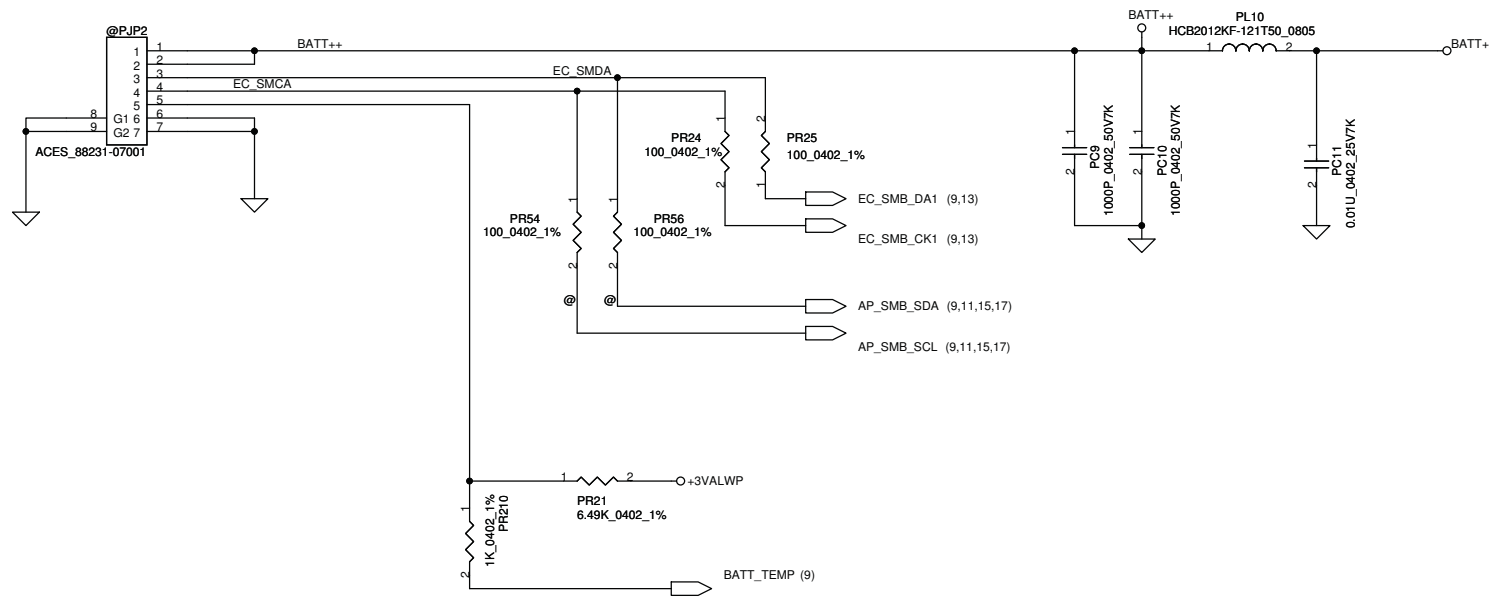
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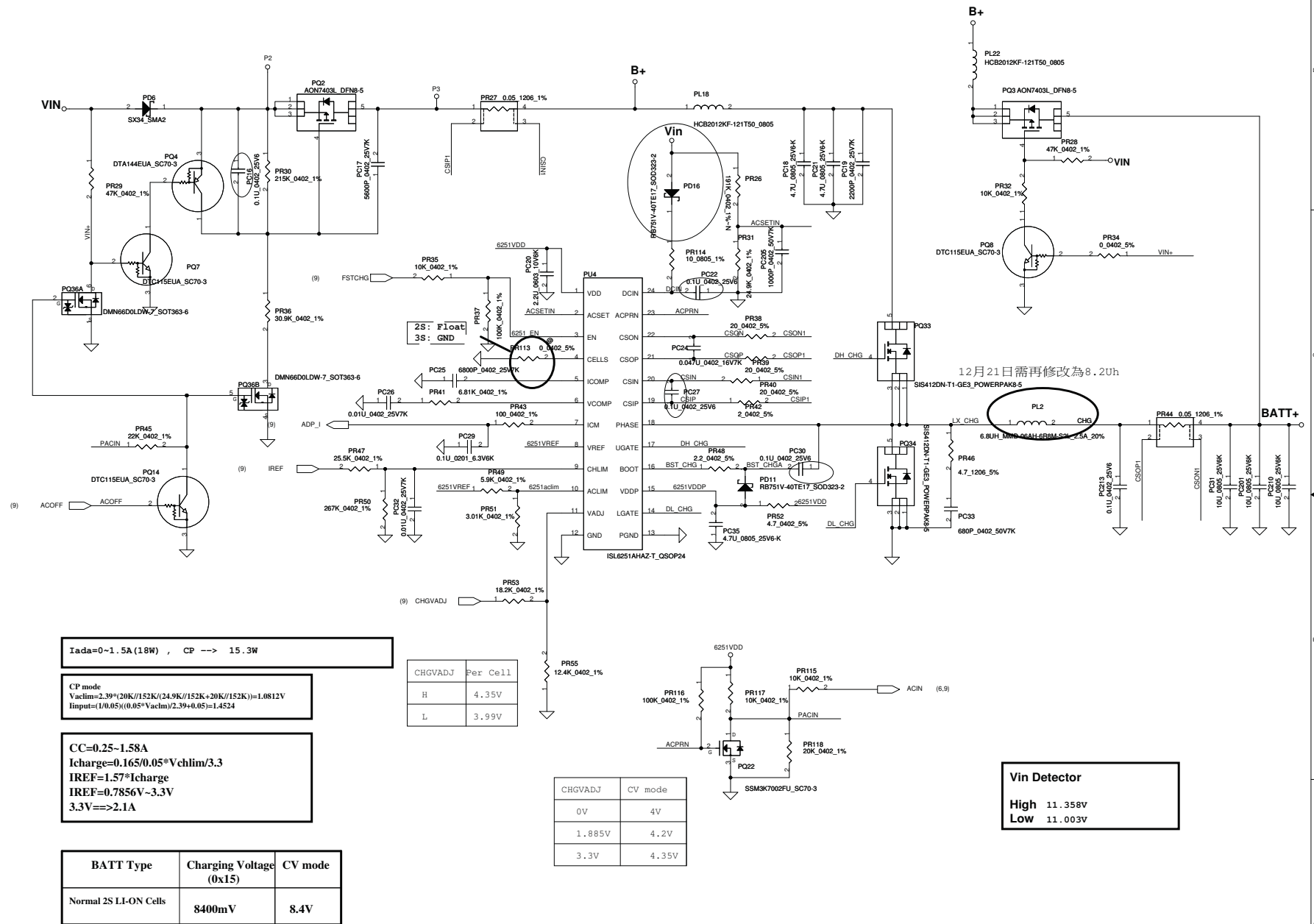
CO-LAY 10/28
要换成(SP02000K200)
11/2 已更换



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Size	Custom	Document Number	Picasso-E	Rev	0.2
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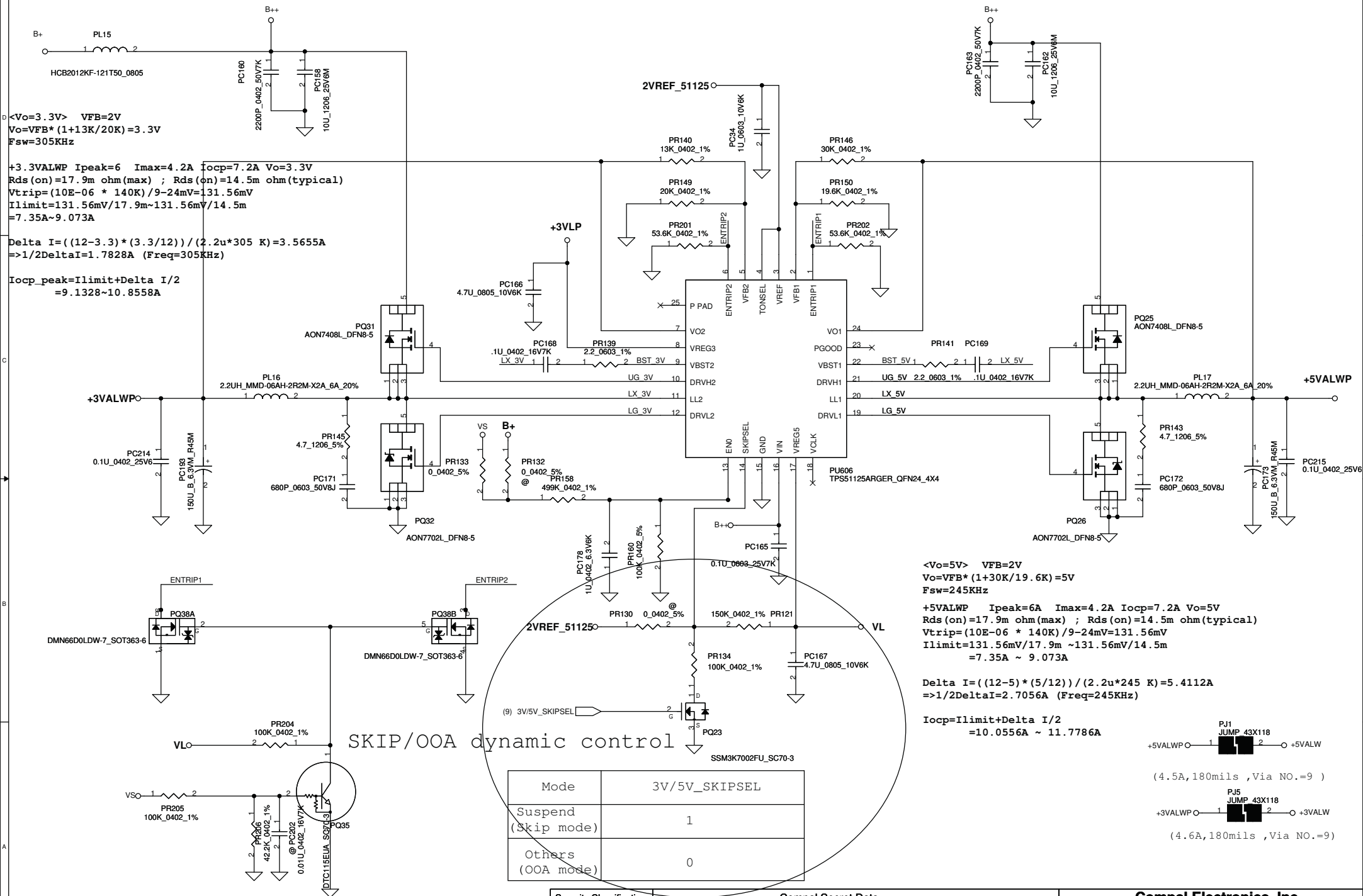


<Vo=3.3V> VFB=2V
Vo=VFB*(1+13K/20K)=3.3V
Fsw=305KHz

+3.3VALWP Ipeak=6 Imax=4.2A Iocp=7.2A Vo=3.3V
Rds(on)=17.9m ohm(max) ; Rds(on)=14.5m ohm(typical)
Vtrip=(10E-06 * 140K)/9-24mV=131.56mV
Ilimit=131.56mV/17.9m~131.56mV/14.5m
=7.35A~9.073A

Delta I=((12-3.3)*(3.3/12))/(2.2u*305 K)=3.5655A
=>1/2DeltaI=1.7828A (Freq=305KHz)

Iocp_peak=Ilimit+Delta I/2
=9.1328~10.8558A



<Vo=5V> VFB=2V
Vo=VFB*(1+30K/19.6K)=5V
Fsw=245KHz

+5VALWP Ipeak=6A Imax=4.2A Iocp=7.2A Vo=5V
Rds(on)=17.9m ohm(max) ; Rds(on)=14.5m ohm(typical)
Vtrip=(10E-06 * 140K)/9-24mV=131.56mV
Ilimit=131.56mV/17.9m~131.56mV/14.5m
=7.35A ~ 9.073A

Delta I=((12-5)*(5/12))/(2.2u*245 K)=5.4112A
=>1/2DeltaI=2.7056A (Freq=245KHz)

Iocp=Ilimit+Delta I/2
=10.0556A ~ 11.7786A

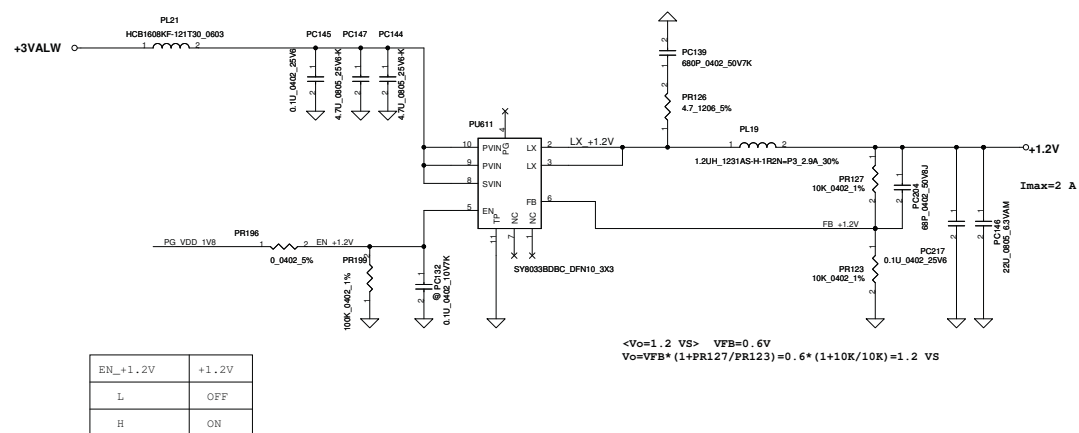
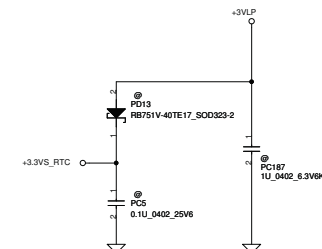
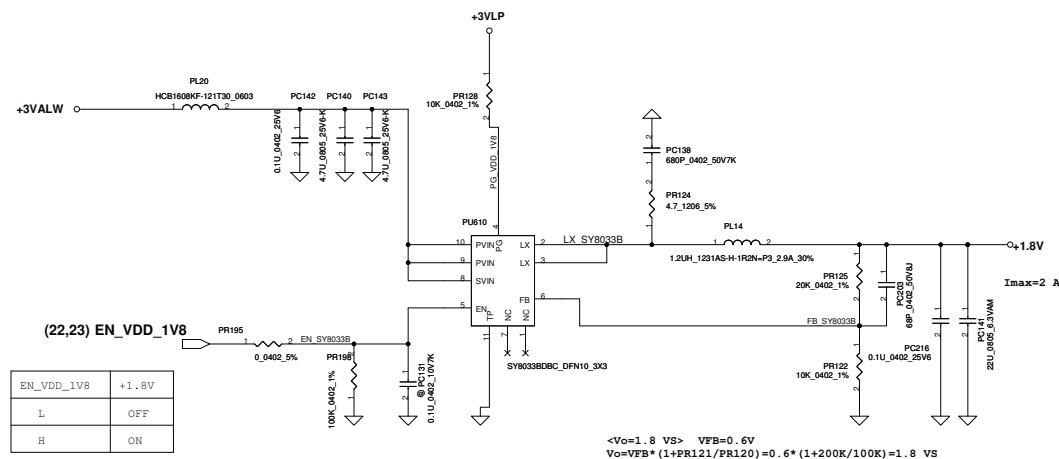
PJ1 JUMP 43X118
+5VALWP 1 2 +5VALW

(4.5A,180mils ,Via NO.=9)

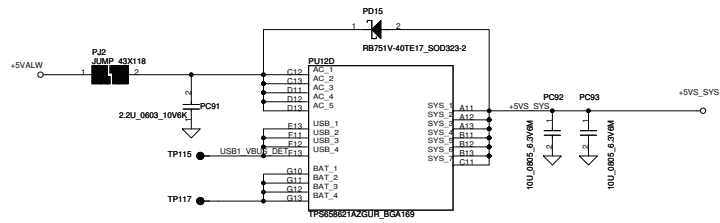
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+3VALWP 1 2 +3VALW

(4.6A,180mils ,Via NO.=9)

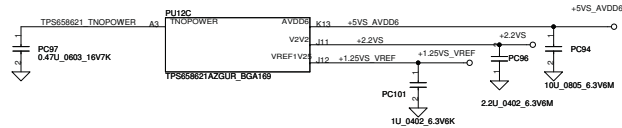
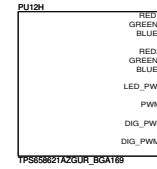
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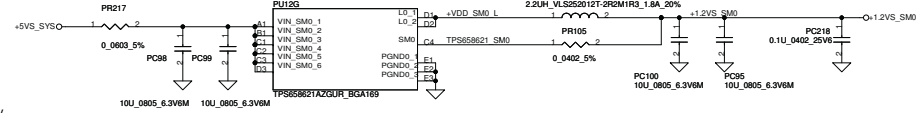
+VDD_SYS=5V
MAX current=1A



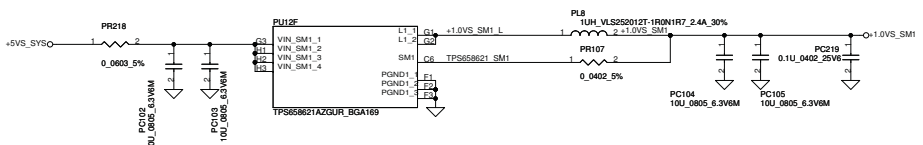
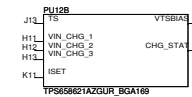
+VDD_AVDD6=5V
MAX current=0.01A

+VDD_V2V2=2.2V
MAX current=0.025A

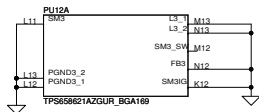
+VDD_VREF1V25=1.25V
MAX current=0.01A



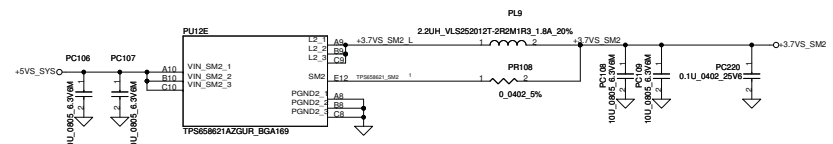
+VDD_SM0=1.2V
MAX current=0.6A



+VDD_SM1=1V
MAX current=1.5A



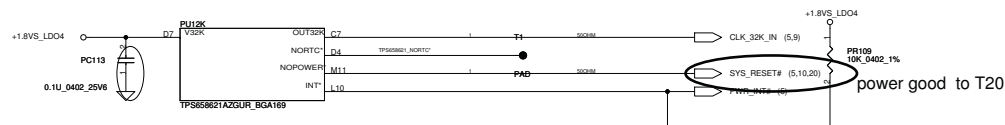
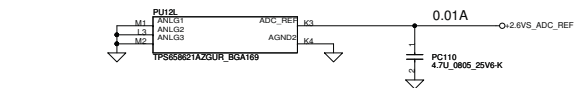
BOOST Converter



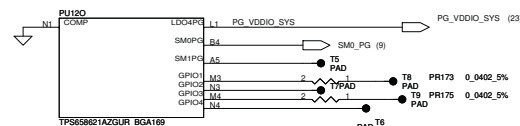
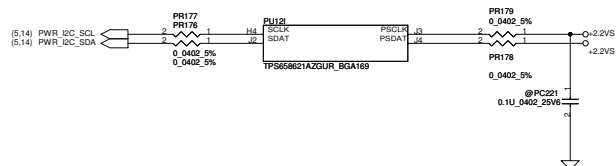
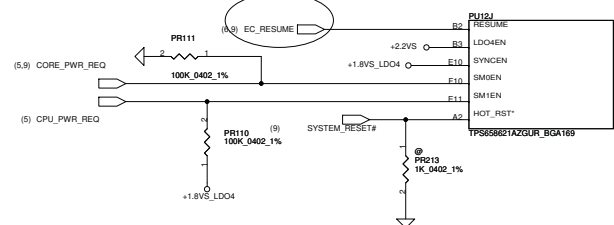
+VDD_SM2=3.7V
MAX current=0.75A

PMU #1

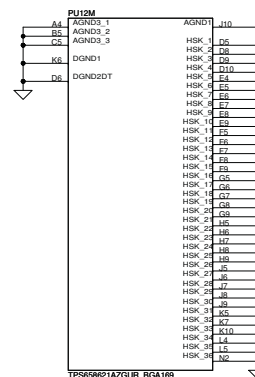
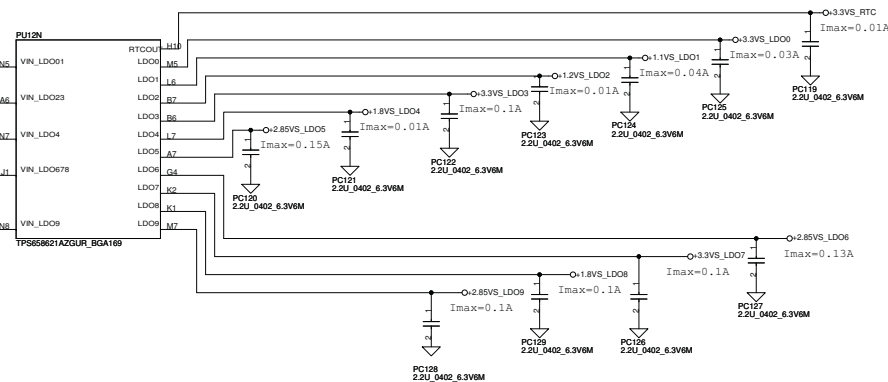
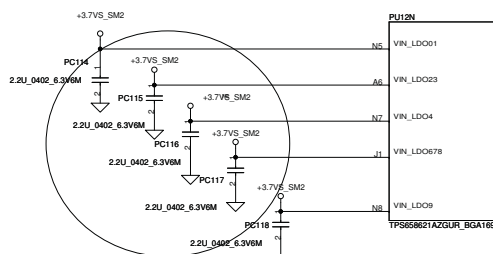
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接到EC
(POWER BUTTON)



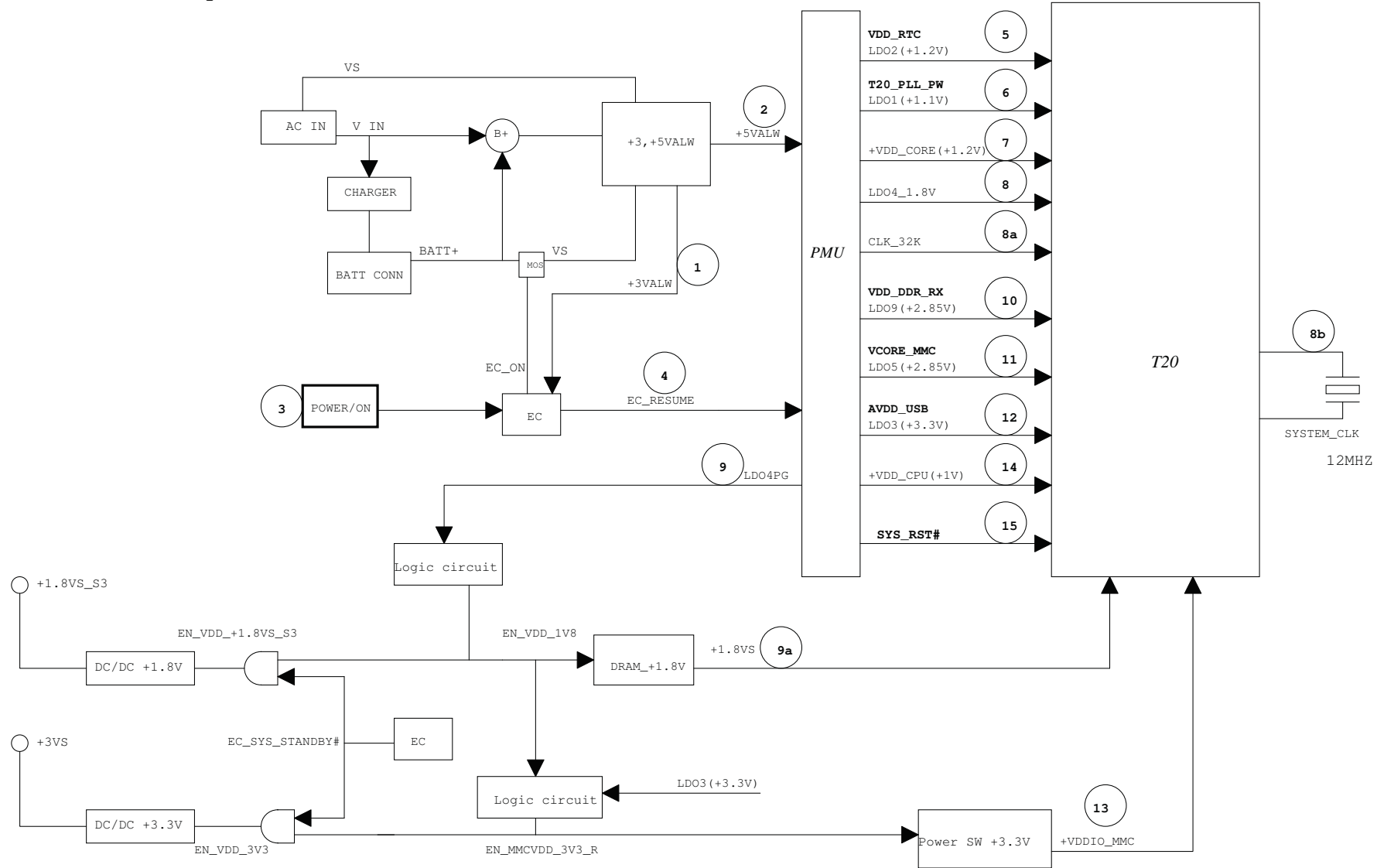
LAYOUT元件擺IC出PIN



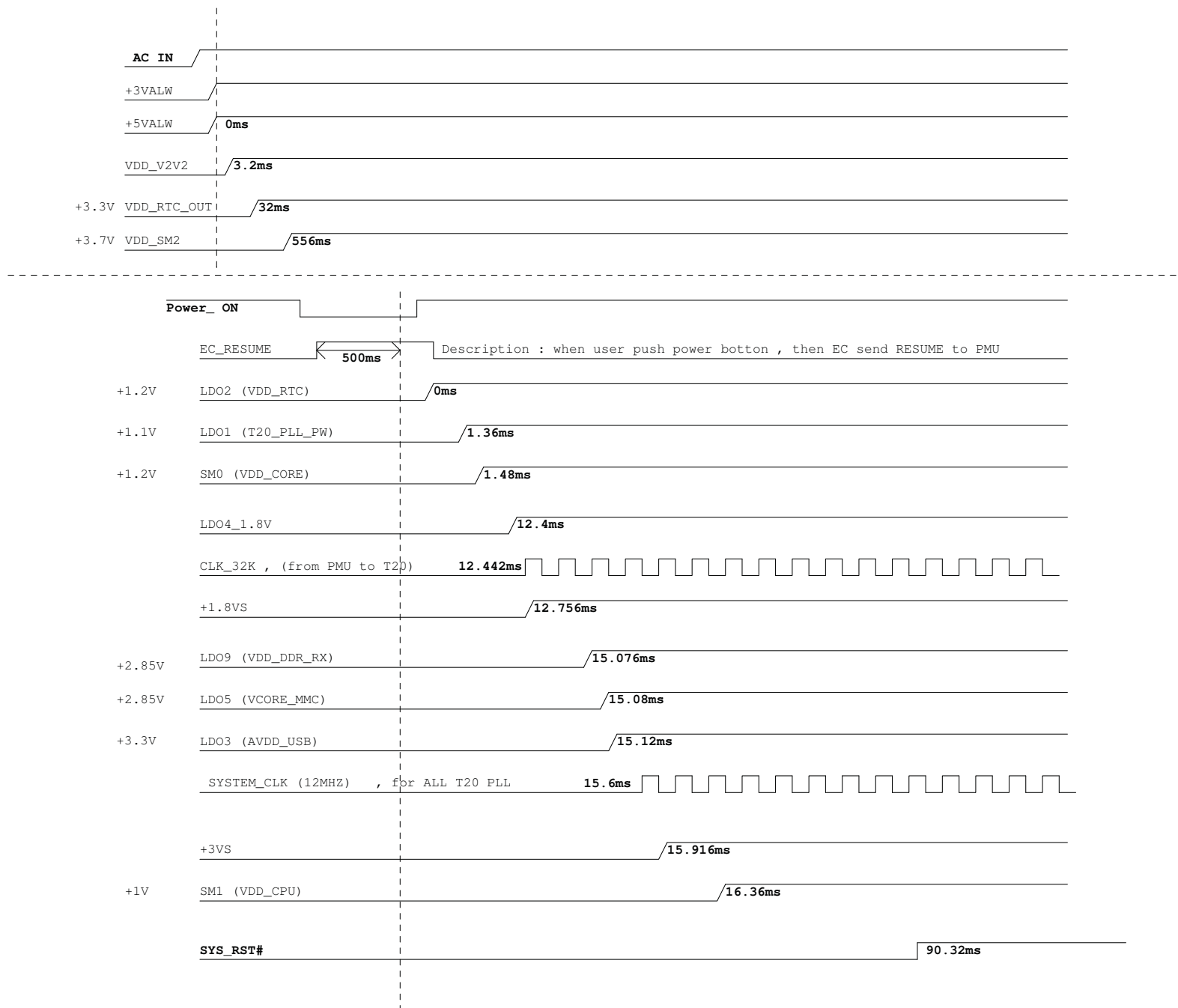
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Size	Document Number	Rev	Picasso-E	
Drawn	Monday, October 03, 2011	12:00	30	of 38

PBJ20 Power Sequence Flow Chart

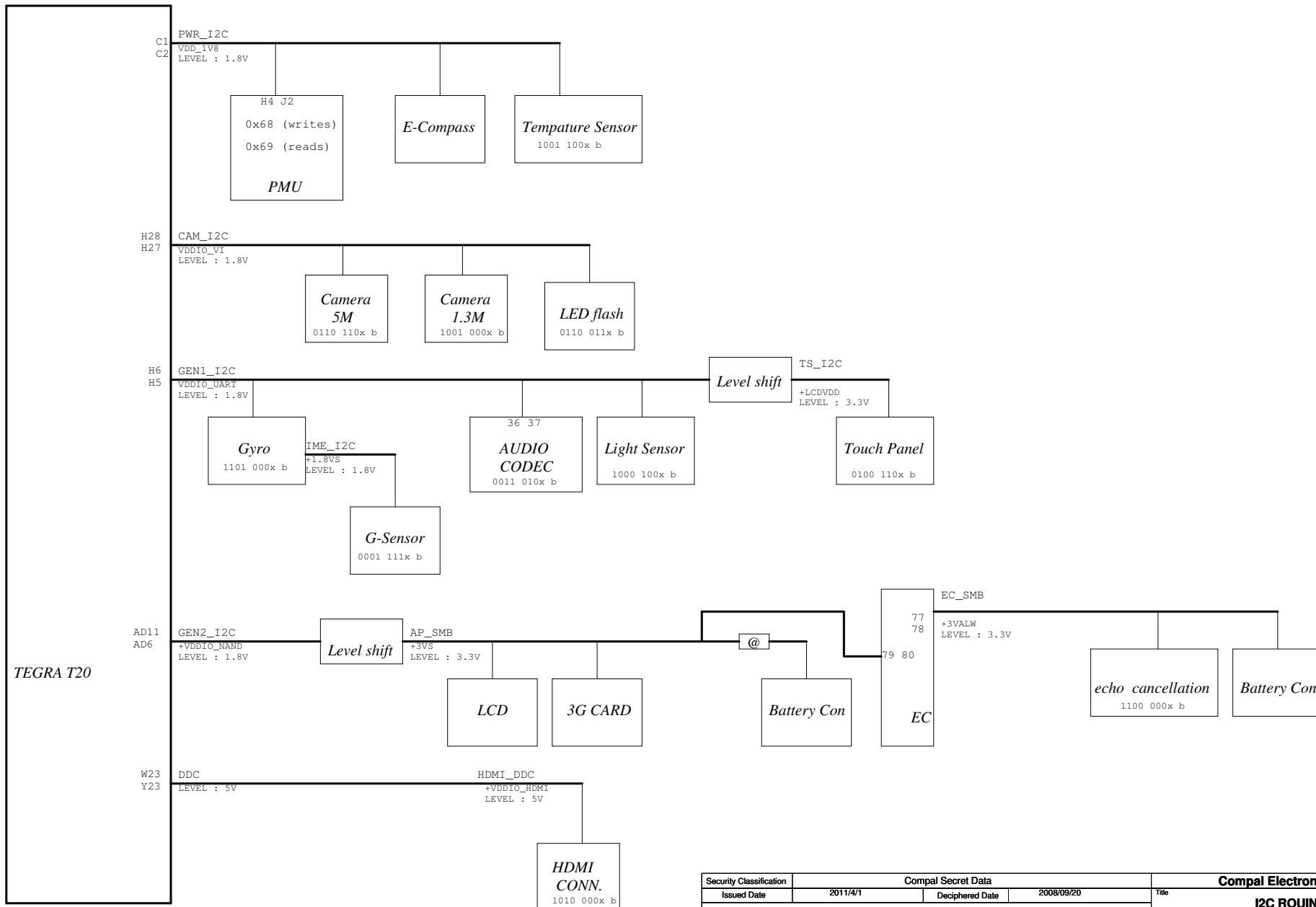


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				Size	Document Number	Rev
				Custom	LA-6531P	0.1
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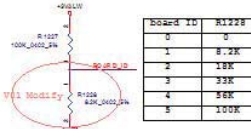

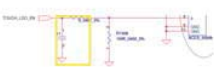
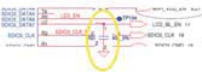


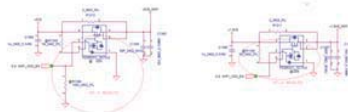


PBJ20 I2C Routing



Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
1	RFQ	Remove Dockin function	NA	P24	Remove PR211, PD7	07/04	NA
2	RFQ	Height limitation	NA	P26	Change PL2 to SH00000MH00	07/04	NA
3	RFQ	Height limitation	NA	P27	Change PL16, PL17 to SH00000FK00	07/04	NA
4	RFQ	Remove LED flash function	NA	NA	Remove LED flash circuit	07/04	NA
5	Miss component update to ispd	Miss component update to ispd	NA	P28	ADD PL20,PL21	8/29	DVT
6	Emi test power noise	Solve power noise		P26	ADD PL18,PL22,PR46,PC33	9/28	PVT
7	Emi test power noise	Solve power noise		P27	ADD PR143,PR145,PC171,PC172	9/28	PVT
8							
9							
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26							

Version V0.2 change to V0.3 list (P.I.R. List)

Item	PCB No	Issues Description	Change & Modify List														
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BOARD ID	R1228																
0	0																
1	8.2K																
2	18K																
3	33K																
4	56K																
5	100K																
2	LA-8111P	EMI: Protect the control board coupling the 168 MHz noise TS_12C_SCL,TS_12C_SDA,TSC_INT# Add 33ohm R and 22P C C1501,C1503,C1504,R1388,R1389,R1390															
3	LA-8111P	EMI: Protect the control board coupling the 168 MHz noise TOUCH_LDO_EN R1031 33ohm and C1502 22P															
4	LA-8111P	EMI: Protect the control board coupling the 168 MHz noise add C1499 22P C in LCD_BL_EN															
5	LA-8111P	EMI: To filter the card reader EMI noise SDIO3_CLK R232 0 --> 33 add C1500 cap 22p															
6	LA-8111P	EMI: To filter the card reader EMI coupling noise C5 Add 22P C in SDIO1_CLK R122 --> 33															
7	LA-8111P	ACER Requirements ADD R1210,R1211 NU R1348,R1349,R1384,r1357, Q58,Q59															
8	LA-8111P	Ecompass Function DEL: NU C1496~c1498,R1377,R1379~R1382,U148															
9	LA-8111P	LED太亮: R14 100 --> 120 R67 300 --> 499															

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